


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
<b>Comments on Statement of Reasons for Allowance</b>	Atty. Docket No. <b>SUZU1720</b>
Applicants <b>Takeshi Ishigaki</b>	
Application No. <b>10/586,951</b>	Date Filed <b>07/25/2006</b>
Title <b>Semiconductor Integrated Circuit Device and Method of Outputting Signals on Semiconductor Integrated Circuit</b>	
Group Art Unit <b>2816</b>	Examiner <b>Rojas, Daniel E.</b>
Confirmation No. <b>3389</b>	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:


<p align="center"><b><u>Certificate of Transmission Under 37 C.F.R. § 1.8</u></b></p> <p>I hereby certify that this correspondence is being deposited electronically with the U.S. Patent and Trademark Office using the United States Patent and Trademark Office's EFS-Web system on March <u>30</u>, 2009.</p> <p align="right">   Janice Pampell </p>
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Applicants appreciate the Examiner's allowance of Claims 6-9 of United States Patent Application No. 10/586,951. Applicants submit the record as a whole makes evident the reasons for allowance and that there are additional reasons for patentability not enumerated by the Examiner. While Applicants agree with the Examiner's reasons for patentability to the extent such reasons are consistent with the record as a whole, Applicants do not acquiesce or agree to any characterization of the claims that place unwarranted limitations or interpretations upon the claims, especially to the extent such limitations or interpretations are inconsistent with the claim language, specification or prior prosecution history in this case.

The Director of the U.S. Patent and Trademark Office is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 50-3183 of Sprinkle IP Law Group.

Respectfully submitted,

**Sprinkle IP Law Group**  
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